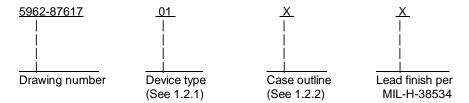
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## 1. SCOPE

- 1.1 Scope. This drawing describes device requirements for class H hybrid microcircuits to be processed in accordance with MIL-H-38534.
  - 1.2 Part or Identifying Number (PIN). The complete PIN shall be as shown in the following example:



1.2.1 <u>Device type(s)</u>. The device type(s) shall identify the circuit function as follows:

Device type	Generic number	Circuit function
01	0006	1.5 amperes, high voltage driver
02	8000	3.0 amperes, high voltage, high current driver

1.2.2 <u>Case outline(s)</u>. The case outline(s) shall be as designated in appendix C of MIL-M-38510, and as follows:

Outline letter Case outline Χ See figure 1 (10-lead, .370" x .185"), can package

1.3 Absolute maximum ratings. 1/

Peak power supply voltage (V <sub>P</sub> ), for less than 0.1 s	+60 V dc
Continuous supply voltage (VS)	+45 V dc
Input voltage (V <sub>IN</sub> )	+5.5 V dc
Input extender current (I <sub>X</sub> )	5.0 mA
Peak output current (I <sub>OUT</sub> ), 50 ms on, 1 s off:	
Device type 01	1.5 A
Device type 02	3.0 A
Continuous output current (I <sub>OUTC</sub> ):	
Device type 01	0.4 A
Device type 02	0.5 A
Thermal resistance, junction-to-case ( $\Theta_{JC}$ )	(See MIL-M-38510, appendix C)
Power dissipation ( $P_D$ ), $T_A = +25^{\circ}$ C:	
Device type 01	750 mW <u>2</u> /
Device type 02	950 mW <u>2</u> /
Junction temperature (T <sub>J</sub> )	+175°C
Storage temperature range	-65°C to +150°C
Lead temperature (soldering, 10 seconds)	+300°C

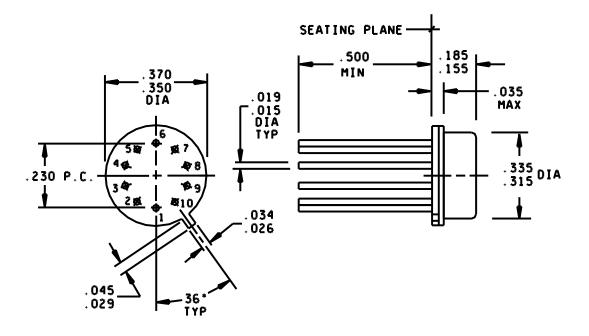
1.4 Recommended operating conditions.

Ambient operating temperature range (T\_A) ----- -55 $^{\circ}$ C to +125 $^{\circ}$ C

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 $<sup>\</sup>overline{1/}$  Unless otherwise specified, all voltages are referenced to ground. 2/ Derate at 210° C/W above  $T_A = +25^{\circ}$  C.

## Case outline X



mm
1111111
4.70
5.84
8.00
8.51
8.89
9.40
12.70

# NOTES:

- Dimensions are in inches.
   Metric equivalents are given for general information only.

FIGURE 1. Case outline.

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#### 2. APPLICABLE DOCUMENTS

2.1 <u>Government specifications and standard</u>. Unless otherwise specified, the following specifications and standard of the issue listed in that issue of the Department of Defense Index of Specifications and Standards specified in the solicitation, form a part of this drawing to the extent specified herein.

### **SPECIFICATIONS**

**MILITARY** 

MIL-M-38510 - Microcircuits, General Specification for.

MIL-H-38534 - Hybrid Microcircuits, General Specification for.

**STANDARD** 

**MILITARY** 

MIL-STD-883 - Test Methods and Procedures for Microelectronics.

(Copies of the specifications and standard required by manufacturers in connection with specific acquisition functions should be obtained from the contracting activity or as directed by the contracting activity.)

2.2 <u>Order of precedence</u>. In the event of a conflict between the text of this drawing and the references cited herein, the text of this drawing shall take precedence.

#### 3. REQUIREMENTS

- 3.1 Item requirements. The individual item requirements shall be in accordance with MIL-H-38534 and as specified herein.
- 3.2 <u>Design, construction, and physical dimensions</u>. The design, construction, and physical dimensions shall be as specified in MIL-H-38534 and herein.
  - 3.2.1 Case outline(s). The case outline(s) shall be in accordance with 1.2.2 herein.
- 3.2.2 <u>Terminal connections and functional diagram</u>. The terminal connections and functional diagram shall be as specified on figure 2.
  - 3.2.3 Switching time waveforms. Switching time waveforms shall be as specified on figure 3.
- 3.3 <u>Electrical performance characteristics</u>. Unless otherwise specified herein, the electrical performance characteristics are as specified in table I and shall apply over the full specified operating temperature range.
- 3.4 <u>Electrical test requirements</u>. The electrical test requirements shall be the subgroups specified in table II. The electrical tests for each subgroup are described in table I.
- 3.5 <u>Marking</u>. Marking shall be in accordance with MIL-H-38534. The part shall be marked with the PIN listed in 1.2 herein. In addition, the manufacturer's PIN may also be marked as listed in QML-38534 (see 6.6 herein).
- 3.6 <u>Manufacturer eligibility</u>. In addition to the general requirements of MIL-H-38534, the manufacturer of the part described herein shall submit for DESC-ECT review and approval electrical test data (variables format) on 22 devices from the initial quality conformance inspection group A lot sample, produced on the certified line, for each device type listed herein. The data should also include a summary of all parameters manually tested, and for those which, if any, are guaranteed.
- 3.7 <u>Certificate of compliance</u>. A certificate of compliance shall be required from a manufacturer in order to be listed as an approved source of supply in QML-38534 (see 6.6 herein). The certificate of compliance submitted to DESC-ECT prior to listing as an approved source of supply shall affirm that the manufacturer's product meets the requirements of MIL-H-38534 and the requirements herein.
- 3.8 <u>Certificate of conformance</u>. A certificate of conformance as required in MIL-H-38534 shall be provided with each lot of microcircuits delivered to this drawing.

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		TABLE I. Electrical perfo	rmance c	haracteristics	<u>S</u> .		
Test	  Symbol 	Conditions -55° C ≤ T <sub>A</sub> ≤ +125° C (unless otherwise specified)	  Device  types	  Group A  subgroups	   <u>Lim</u>     Min	iits     Max	   Unit 
Logical "1" input voltage	i  V <sub>IH</sub> 	  10 V ≤ V <sub>CC</sub> ≤ 45 V 	  01,02 	   1,2,3 	   2.0 		   V 
Logical "0" input voltage	i  V <sub>IL</sub> 	-     	     	     	     	   0.8 	       
Logical "1" output voltage	IV <sub>OH</sub>	  V <sub>CC</sub> = 10 V, V <sub>IN</sub> = 2 V,  I <sub>OUT</sub> = 150 mA	01	   1,2,3 	   8.8 	     	-!     
	 	  V <sub>CC</sub> = 28 V, V <sub>IN</sub> = 2 V,  I <sub>OUT</sub> = 400 mA		     	   26.5 	     	       
	 	  V <sub>CC</sub> = 28 V, V <sub>IN</sub> = 2 V,  I <sub>OUT</sub> = 800 mA	02	-       	   26.5 	     	-       
	 	  V <sub>CC</sub> = 45 V, V <sub>IN</sub> = 2 V,  I <sub>OUT</sub> = 1.6 A		     	   43.0 	     	       
Logical "0" output voltage	IV <sub>OL</sub>	$ V_{CC}  = 45 \text{ V}, V_{IN} = 0.8 \text{ V},$ $ R_L  = 1 \text{ k}\Omega$	   01     02	   1,2,3 	    	   10     100	   mV 
Logical "1" input	i II <sub>IH</sub>	  V <sub>CC</sub> = 45 V, V <sub>IN</sub> = 2.4 V	01,02	   1,2,3 	   	     5.0	   μΑ 
		  V <sub>CC</sub> = 45 V, V <sub>IN</sub> = 5.5 V	  01,02 	   	   	   100 	   
Logical "0" input current	  I <sub>IL</sub>	  V <sub>CC</sub> = 45 V, V <sub>IN</sub> = 0.4 V	01,02	   1,2,3 	  -1.0 	   	   mA 
Input breakdown <u>1</u> / voltage	  B <sub>VIN</sub> 	   	  01,02 	   1,2,3 	   5.5 	   	   V 
"Off" power supply current	  I <sub>CC</sub> 	  V <sub>CC</sub> = 45 V, V <sub>IN</sub> = 0.8 V 	01	   1,2,3 <u> </u>	   	   2.0 	   mA <u> </u>
	(off)   	  V <sub>CC</sub> = 45 V, V <sub>IN</sub> = 0 V	   02 	   	   	   2.0 	   <u> </u>
"On" power supply current	  I <sub>CC</sub>    (on)	$ V_{CC}  = 45 \text{ V}, V_{IN} = 2 \text{ V}, $ $ I_{OUT}  = 0 \text{ mA}$	  01,02 	   1,2,3 	     	   8.0 	     

See footnotes at end of table.

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		TABLE I. <u>Electrical performan</u>	ce charact	teristics - Cor	ntinued.	•	
Test	  Symbol 	Conditions $\begin{array}{c c} & \text{Conditions} \\ & -55^{\circ}\text{C} \leq \text{T}_{A} \leq +125^{\circ}\text{C} \\ & \text{Conditions} \end{array}$	  Device  types	  Group A  subgroups		mits	 _  Unit 
Rise time 2/	  t <sub>r</sub>	(unless otherwise specified)   $ V_{CC} = 28 \text{ V}, V_{IN} = 5 \text{ V},  R_L = 82\Omega, see figure 3}$	01	  9,10,11 	Min       	<u>Max</u>    300 	   ns 
		$ V_{CC}  = 28 \text{ V}, V_{IN} = 5 \text{ V},$ $ R_L  = 39\Omega \text{ see figure } 3$	02	-!     	       	  500 	-      
Fall time 2/	  t <sub>f</sub> 	$ V_{CC}  = 28 \text{ V}, V_{IN} = 5 \text{ V},$ $ R_L  = 82\Omega$ , see figure 3	   01 	9,10,11	     	   1.5 	   µs 
		$ V_{CC}  = 28 \text{ V}, V_{IN} = 5 \text{ V},$ $ R_L  = 39\Omega$ , see figure 3	02	9, 11	 	4.0	-    _  
Turn on time 2/	l t <sub>on</sub>	$ V_{CC}  = 28 \text{ V}, V_{IN} = 5 \text{ V},$ $ R_L  = 82\Omega$ , see figure 3	     01 	10    9,10,11 	     	6.0     1.0 	_      
	ļ	ļ		_[	<u></u>	-	_!

 $|V_{CC}| = 28 \text{ V}, V_{IN} = 5 \text{ V},$  $|R_L| = 39\Omega$ , see figure 3

 $|V_{CC}| = 28 \text{ V}, V_{IN} = 5 \text{ V},$  $|R_L| = 82\Omega$ , see figure 3

 $|V_{CC}| = 28 \text{ V}, V_{IN} = 5 \text{ V},$  $|R_L| = 39\Omega$ , see figure 3

02

01

02

9,10,11

9, 11 10

1.0

5.2

10

14

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Turn off time 2/

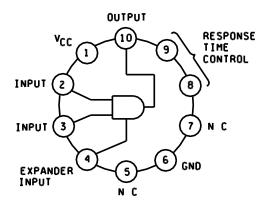
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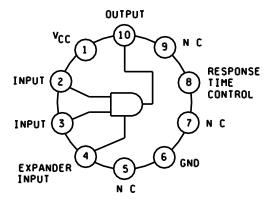
Parameter shall be tested as part of device initial characterization and after design and process changes. Parameter shall be guaranteed to the limits specified in table I for all lots not specifically tested.

Subgroups 10 and 11 shall be tested as part of device initial characterization and after design and process changes.

Parameter shall be guaranteed to the limits specified in table I for all lots not specifically tested.

<u>Device type 01</u> <u>Device type 02</u>





CASE IS ELECTRICALLY ISOLATED

CASE IS ELECTRICALLY ISOLATED

FIGURE 2. Terminal connections and functional diagram (top view).

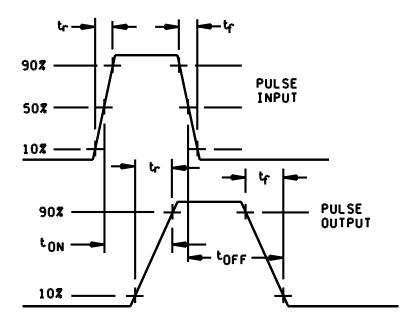


FIGURE 3. Switching time waveforms.

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- 4. QUALITY ASSURANCE PROVISIONS
- 4.1 <u>Sampling and inspection</u>. Sampling and inspection procedures shall be in accordance with MIL-H-38534.
- 4.2 <u>Screening</u>. Screening shall be in accordance with MIL-H-38534. The following additional criteria shall apply:
  - a. Burn-in test, method 1015 of MIL-STD-883.
    - (1) Test condition A, B, C, or D using the circuit submitted with the certificate of compliance (see 3.7 herein).
    - (2)  $T_A$  as specified in accordance with table I of method 1015 of MIL-STD-883.
  - b. Interim and final electrical test parameters shall be as specified in table II herein, except interim electrical parameter tests prior to burn-in are optional at the discretion of the manufacturer.
- 4.3 Quality conformance inspection. Quality conformance inspection shall be in accordance with MIL-H-38534 and as specified herein.
  - 4.3.1 Group A inspection. Group A inspection shall be in accordance with MIL-H-38534 and as follows:
    - a. Tests shall be as specified in table II herein.
    - b. Subgroups 4, 5, 6, 7, and 8 shall be omitted.
  - 4.3.2 Group B inspection. Group B inspection shall be in accordance with MIL-H-38534.
  - 4.3.3 Group C inspection. Group C inspection shall be in accordance with MIL-H-38534 and as follows:
    - a. End-point electrical parameters shall be as specified in table II herein.
    - b. Steady-state life test conditions, method 1005 of MIL-STD-883.
      - (1) Test condition A, B, C, or D using the circuit submitted with the certificate of compliance (see 3.7 herein).
      - (2)  $T_A$  as specified in accordance with table I of method 1005 of MIL-STD-883.
      - (3) Test duration: 1,000 hours, except as permitted by method 1005 of MIL-STD-883.
  - 4.3.4 Group D inspection. Group D inspection shall be in accordance with MIL-H-38534.

TABLE II. Electrical test requirements.

MIL-STD-883 test requirements	Subgroups     Subgroups     (per method     5008, group A     test table)
  Interim electrical parameters	   1
  Final electrical test parameters	1*, 2, 3, 9
  Group A test requirements	
	1, 2, 3

<sup>\*</sup> PDA applies to subgroup 1.

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- 5. PACKAGING
- 5.1 Packaging requirements. The requirements for packaging shall be in accordance with MIL-H-38534.
- 6. NOTES
- 6.1 <u>Intended use</u>. Microcircuits conforming to this drawing are intended for original equipment design applications and logistic support of existing equipment.
- 6.2 <u>Replaceability</u>. Microcircuits covered by this drawing will replace the same generic device covered by a contractor-prepared specification or drawing.
- 6.3 <u>Configuration control of SMD's</u>. All proposed changes to existing SMD's will be coordinated with the users of record for the individual documents. This coordination will be accomplished in accordance with MIL-STD-481 using DD Form 1693, Engineering Change Proposal (Short Form).
- 6.4 Record of users. Military and industrial users shall inform Defense Electronics Supply Center when a system application requires configuration control and the applicable SMD. DESC will maintain a record of users and this list will be used for coordination and distribution of changes to the drawings. Users of drawings covering microelectronics devices (FSC 5962) should contact DESC-ECT, telephone (513) 296-6047.
- 6.5 <u>Comments</u>. Comments on this drawing should be directed to DESC-ECT, Dayton, Ohio 45444, or telephone (513) 296-5374.
- 6.6 <u>Approved sources of supply</u>. Approved sources of supply are listed in QML-38534. Additional sources will be added to QML-38534 as they become available. The vendors listed in QML-38534 have agreed to this drawing and a certificate of compliance (see 3.7 herein) has been submitted to and accepted by DESC-ECT.

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